

AMENDMENTS TO THE CLAIMS:

Kindly amend claims 1, 4 and 8-14, as shown below. Please add new claim 15, as shown below.

This listing of claims will replace all prior versions and listings of claims in the Application:

Claim 1 (currently amended): A display device comprising:

a first input terminal ~~which receives~~ for receiving an analog image signal;

→ a second input terminal ~~which receives~~ for receiving a digital image signal;

an analog-to-digital converter connected to said first input terminal;

a first switch ~~which selects~~ for selecting an output between a digital signal outputted from said analog-to-digital converter and a digital signal inputted to said second input terminal; and

a gradation circuit ~~which converts~~ for converting a digital signal outputted from said first switch into a signal indicative of a level of pseudo gradation, said gradation circuit comprising:

an error diffusion circuit ~~which converts~~ for converting said digital signal outputted from said first switch into a signal indicative of a level of pseudo gradation by an error diffusion method;

a dither pattern circuit ~~which converts~~ for converting said digital signal outputted from said first switch into a signal indicative of a level of pseudo gradation by a dithering method; and

a second switch ~~which selects~~ for converting for output between an output signal of said error diffusion circuit and an output signal of said dither ~~patter~~ pattern circuit.

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Claim 2 (original): The display device according to claim 1, wherein said first switch is operatively switched by a user.

Claim 3 (original): The display device according to claim 1, wherein said second switch is operatively switched by a user.

Claim 4 (currently amended): The display device according to claim 1, wherein said gradation circuit comprises a noise detector ~~which detects~~ for detecting noise of an output signal of said first switch, and a switch controller ~~which controls~~ for controlling said second switch in accordance with a result of detection by said noise detector.

Claim 5 (original): The display device according to claim 4, wherein said noise detector determines whether lower bits of an output signal of said first switch include noise or not.

Claim 6 (original): The display device according to claim 5, wherein when it has been determined by said noise detector that noise is included, said switch controller causes said second switch to output an output signal of said error diffusion circuit.

Claim 7 (original): The display device according to claim 5, wherein when it has been determined by said noise detector that noise is not included, said switch controller causes said second switch to output an output signal of said dither pattern circuit.

Claim 8 (currently amended): The display device according to claim 1, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

Claim 9 (currently amended): The display device according to claim 2, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

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Claim 10 (currently amended): The display device according to claim 3, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

Claim 11 (currently amended): The display device according to claim 4, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

Claim 12 (currently amended): The display device according to claim 5, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

Claim 13 (currently amended): The display device according to claim 6, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

Claim 14 (currently amended): The display device according to claim 7, further comprising:

a driver ~~which~~ for receiving an output signal of said second switch; and
a display panel to be driven by said driver.

Claim 15 (new): A video display device capable of receiving a digital video signal converted from an analog input video signal and a digital input video signal, said display comprising a graduation circuit wherein:

a pseudo intermediate gradation process can be executed in both an error diffusion method and a dithering method, and
when said digital video signal converted from said analog input video signal is input, said pseudo intermediate gradation process in said error diffusion method is selected, whereas when

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said digital input video signal is input, said pseudo intermediate gradation process in said
dithering method is selected.

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